

WE CLAIM:

1. A nonvolatile memory system comprising:

a control device; and

a plurality of nonvolatile memory devices each of which has a plurality of memory cells and a plurality of data latches;

wherein said control device supplies a plurality of commands to said nonvolatile memory devices, which commands comprise a write command, a first read command and a second read command,

wherein, when said control device supplies said write command with write address information and data for storing in said nonvolatile memory device, said nonvolatile memory device stores data to said data latches, stores said data in said data latches to ones of said memory cells and verifies whether said write data has been stored in said memory cells or not,

wherein, when said control device supplies said first read command with read address information, said nonvolatile memory device reads data stored in ones of said memory cells to said data latches and outputs said data in said data latches to said control device, and

wherein, when said control device supplies said second read command, said nonvolatile memory device outputs data in said data latches to said control device.

2. A nonvolatile memory system according to claim 1,

wherein, in an operation of said write command, said nonvolatile memory device supplies information to said control device as to whether the data storing is a success or a failure, and

wherein said control device is capable of supplying said second read command when said control device is supplied with said information indicating the data storing being a failure.

3. A nonvolatile memory system according to claim 2,

wherein said control device is capable of supplying said write command and received data from said nonvolatile memory device by said second read command to another nonvolatile memory device.

4. A nonvolatile memory system according to claim 2,

wherein said control device is capable of supplying said write command with address information different from said write address information and received data from said nonvolatile memory device by said second read command.